



Chips JU 2025 Information Day
Roma, 1 Aprile 2025

Chips Mirror Group Italy 2025

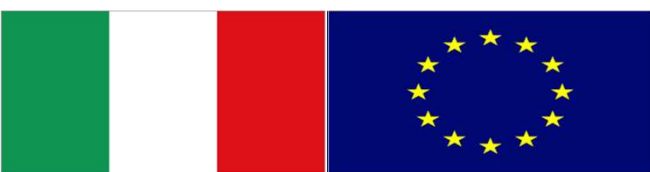
Livio Baldi



What is it?



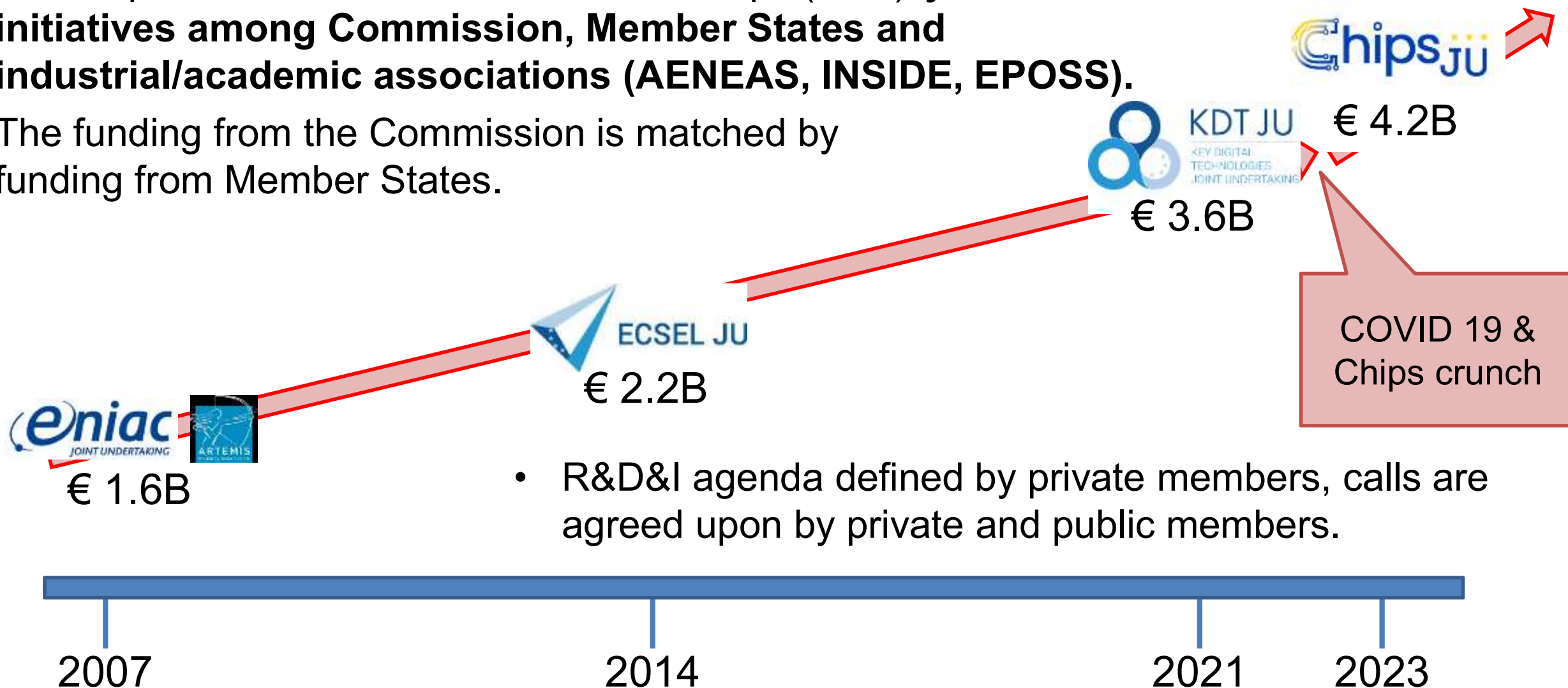
- The Chips Mirror Group Italy is the follow-up of ECSEL Italy, founded in 2015 with the purpose:
 - ✓ To facilitate / coordinate the participation of Italian industrial and academic research organizations to the Joint Technology Initiatives on semiconductors;
 - ✓ To promote the support by Italian Public Authorities,
 - ✓ To coordinate with similar initiatives in Europe.
- Chips Mirror Group Italy has the administrative support of AEIT.
- Chips Mirror Group Italy is funded via participation to European Projects, currently to the ICOS CSA.



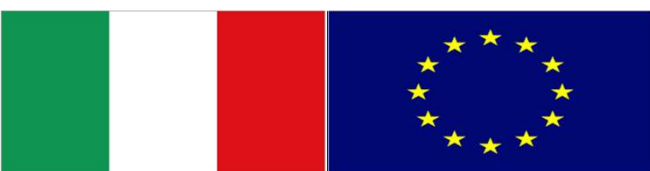
The Joint Technology Initiatives (JTI)



- JTI are part of the Public-Private Partnerships (PPP): **joint R&D&I initiatives among Commission, Member States and industrial/academic associations (AENEAS, INSIDE, EPOSS).**
- The funding from the Commission is matched by funding from Member States.



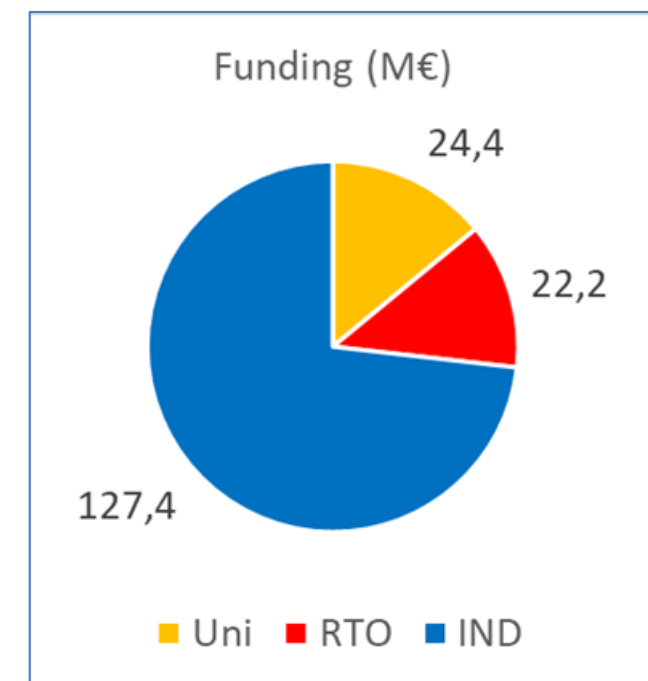
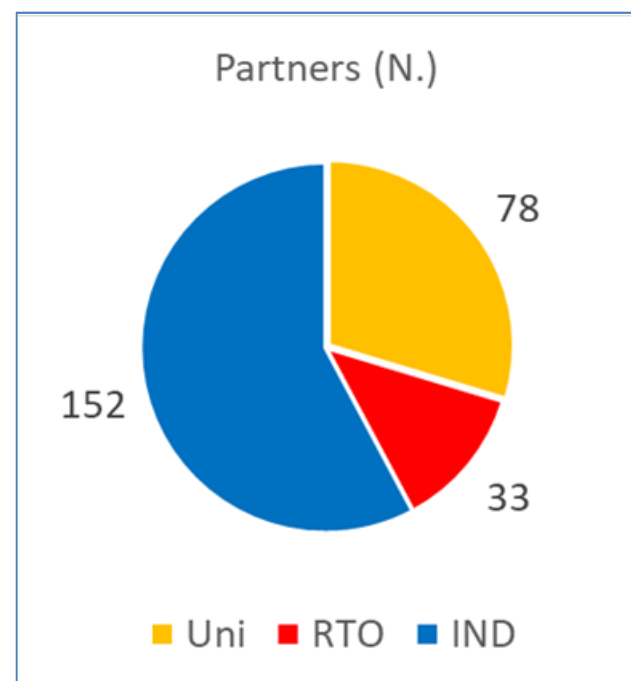
- R&D&I agenda defined by private members, calls are agreed upon by private and public members.



Italian participation (2014-2020)



	RIA Italia	IA Italia	Totale IT	Totale call
2014	4	3	7	12
2015	3	2	5	12
2016	7	3	10	14
2017	6	3	9	14
2018	5	3	8	13
2019	6	4	10	14
2020	6	4	10	14
Totale	37	22	59	93



In the period 2014-2020:

- there have been 279 Italian participations by 139 partners
- there were Italian partners in 59 research projects over 93 launched
- among the top 5 Italian participants, 4 are Universities and RTO
- in the last two years of ECSEL, Italy has participated in 70% of projects
- In the KDT and Chips JU RIA calls, Italy participated in 22 projects, for €31.5 M.



2022: European Chips Act



European Semiconductor Board (Governance)

Chips JU:

- Calls
- Pilot Lines
- Competence Centers

Pillar 1

Chips for Europe Initiative

- Initiative on infrastructure building in synergy with the EU's research programmes
- Support to start-ups and SMEs

Pillar 2

Security of Supply

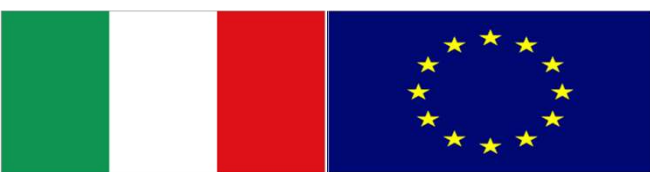
- First-of-a-kind semiconductor production facilities

Attract new fabs
in Europe

Pillar 3

Monitoring and Crisis Response

- Monitoring and alerting
- Crisis coordination mechanism with MS
- Strong Commission powers in times of crisis



Chips for Europe



ECS R&I Calls

Pilot Lines



Competence Centres



Design Platform



Topic	Description	Indicative JU Budget M€
HORIZON-JU-Chips-2025-RIA-SUP	Support for start-ups and SMEs	120
HORIZON-JU-Chips-2025-CSA	Pan-European infrastructure for Chips Design Innovation	12
HORIZON-JU-Chips-2025-IA-FDA	Open-source FDA tools development	20
HORIZON-JU-Chips-2025-TPA-QAC1	Establishing Framework Partnership Agreements for developing Quantum Chip Technology for stability Pilots	0
Lab to Fab	Lab to Fab accelerator European ecosystem for chiplet integration	90
Other Activities		
HORIZON-JU-Chips-2025-SGA-QAC1	Supporting developing Quantum Chip Technology for stability Pilots	90
HORIZON-JU-Chips-2025-SGA-QAC2	Supporting developing Quantum Chip Technology for high-quality Integrated Pilot	20
TOTAL		372



Italy in the Chips Act



Nasce a Pavia la Fondazione Chips.IT
Il Centro italiano per il design dei circuiti integrati a semiconduttori, coordinerà attività di ricerca e design con attori pubblici e privati, mettendo a disposizione attrezzature e software di ultima generazione.



Contratti di sviluppo – Nuovo sportello Semiconduttori



3,3 Miliardi di euro di contributi a fondo perduto e finanziamento agevolato per sostenere la crescita e lo sviluppo tecnologico della catena di approvvigionamento dei semiconduttori



Un nuovo fab di STM a Catania per i semiconduttori a largo gap.



Un nuovo sito di Silicon Box a Novara per advanced packaging.



What can we do for you?



- Disseminate information on Chips JU events and calls.
- Keep informed on future Chips JU and Chips Act initiatives and on running project proposals
- Collect project ideas and presentation of partners' competences for future brokerage events.
- Provide support and advice in the search for projects partners and in proposal preparation.





Follow us on the web sites

In addition to the previous web site,
hosted by AEIT at:

<https://www.aeit.it/ecsel>,

a new web site is available at:

<https://www.chips-mgi.it/>





Thank you!

<http://www.aeit.it/ecsel>

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Steering Committee

